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Serial No.:

10/083,261

Examiner: George Y. Wang

Filed:

February 25, 2002

Group Art Unit: 2871

For:

THIN FILM TRANSISTOR ARRAY SUBSTRATE USING LOW DIELECTRIC INSULATING LAYER AND METHOD OF FABRICATING THE SAME

Mail Stop RCE

Commissioner for Patents PO Box 1450

Alexandria VA: 22313-1450

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Respectfully submitted,

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Fax: (516) 692-8889

David L. Heath Reg. No. 46,763 Attorney for Applicant

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Mail Stop RCE, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on

December 3, 2004.

Dated: 12304

David I. Hooth

## **PATENT**



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Choi, et al.

Examiner: George Y. Wang

Serial No:

10/083,261

Group Art Unit: 2871

Filed:

February 25, 2002

Docket: 8071-12 (OPP 011059US)

For:

THIN FILM TRANSISTOR ARRAY SUBSTRATE USING LOW DIELECTRIC INSULATING LAYER AND METHOD

OF FABRICATING THE SAME

**Mail Stop RCE** 

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **AMENDMENT**

Sir:

Prior to examination of the above-referenced application, please amend the claims as follows. No new matter is introduced.

## CERTIFICATE OF MAILING 37 C.F.R. §1.8(a)

I hereby certify that this correspondence (and any document referred to as being attached or enclosed) is being deposited with the United States Postal Service as first class mail, postage paid in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on **December 3, 2004.** 

Dated: 12/3/04

David L. Heath